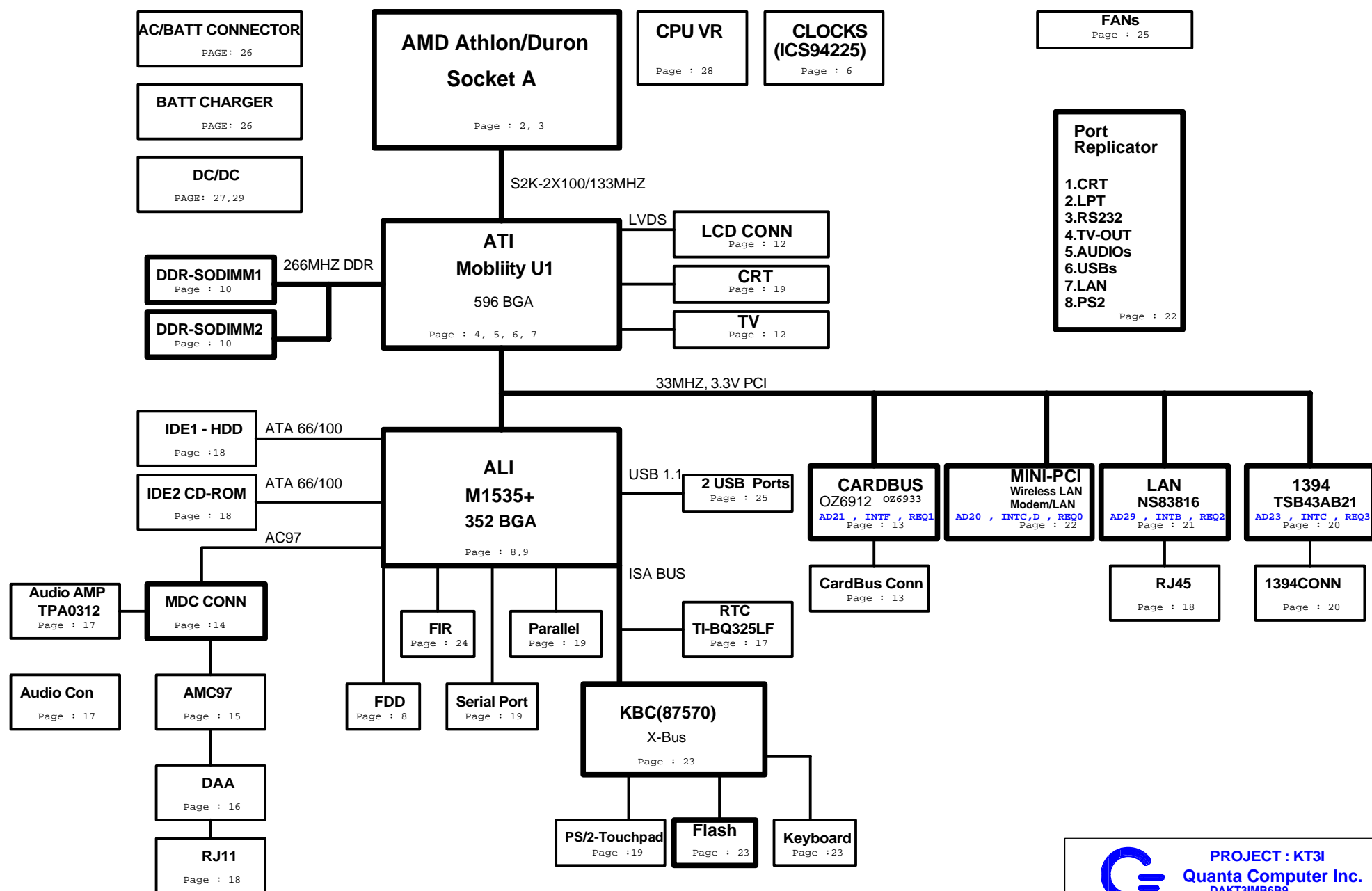
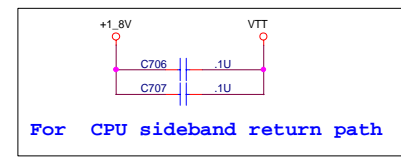
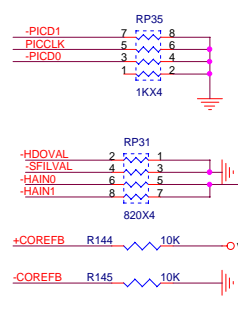
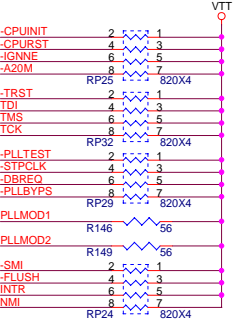
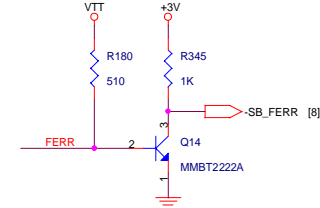
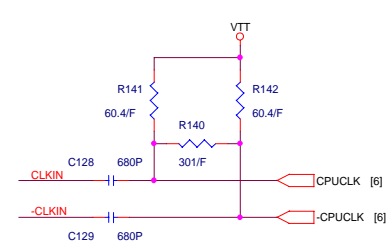
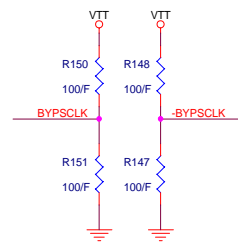
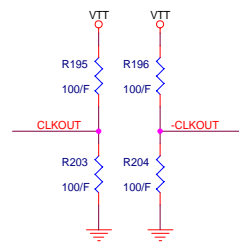
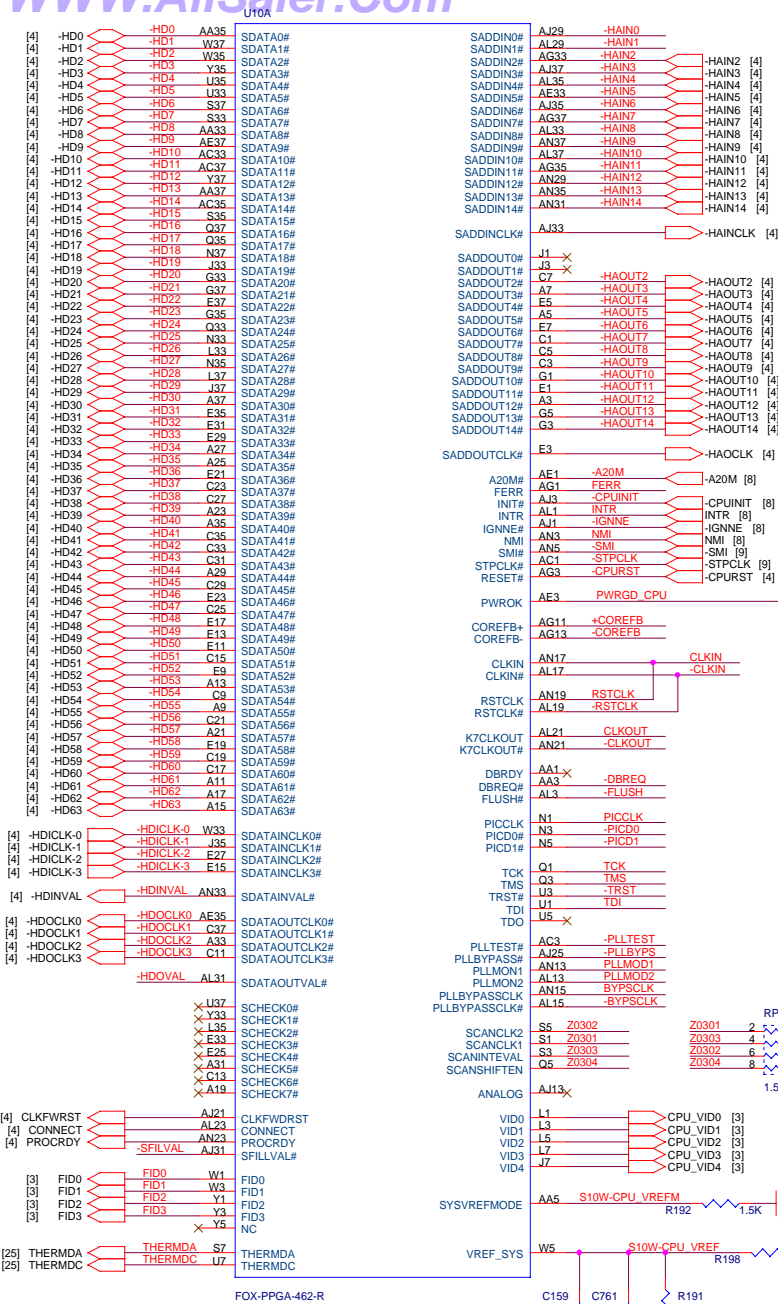


T2.2



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Size B	Document Number	Rev 3D
Schematic Block Diagram		
Date: Wednesday, March 19, 2003	Sheet 1 of 29	



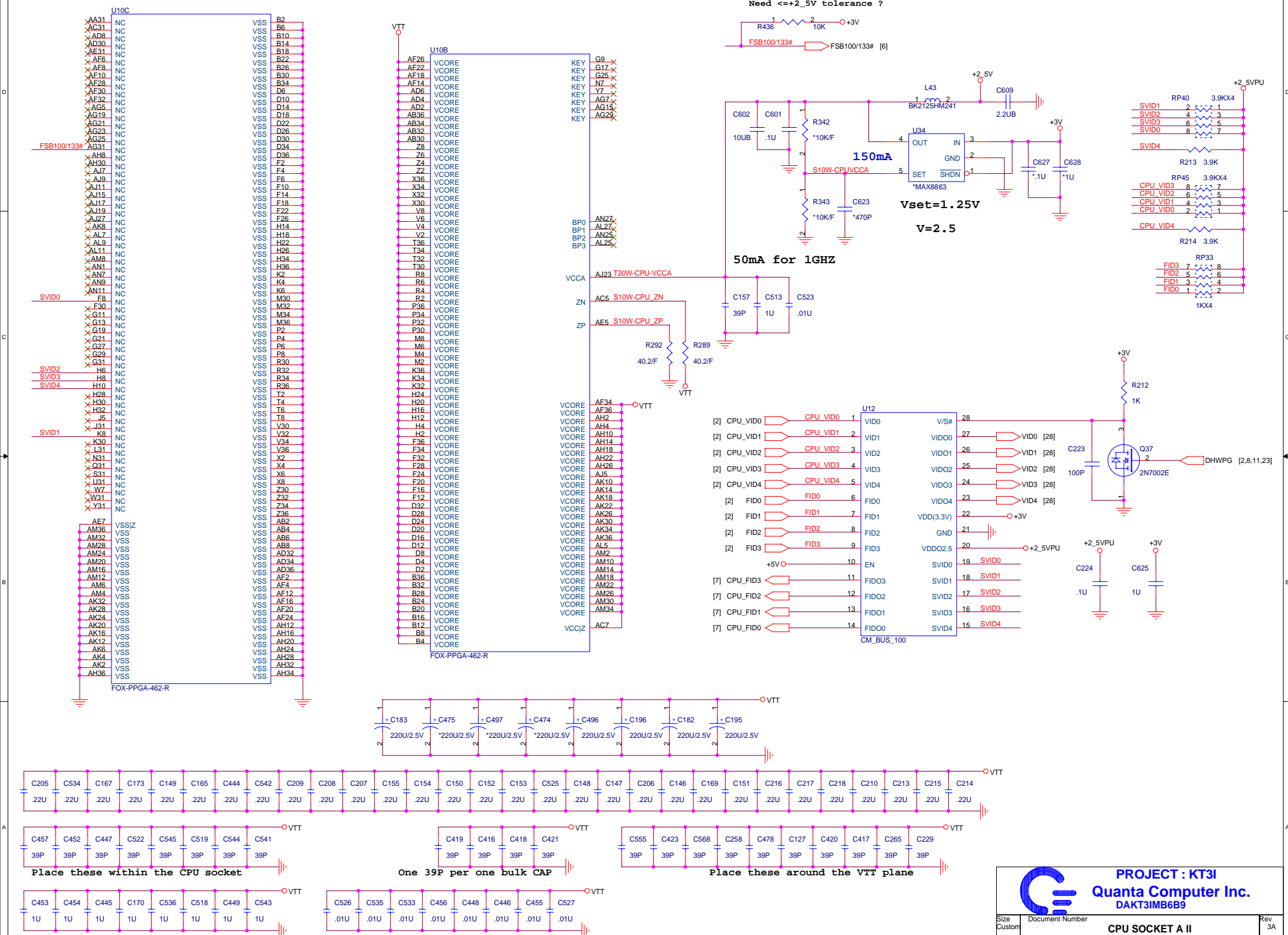
SYSCCLK Multiplier Combinations

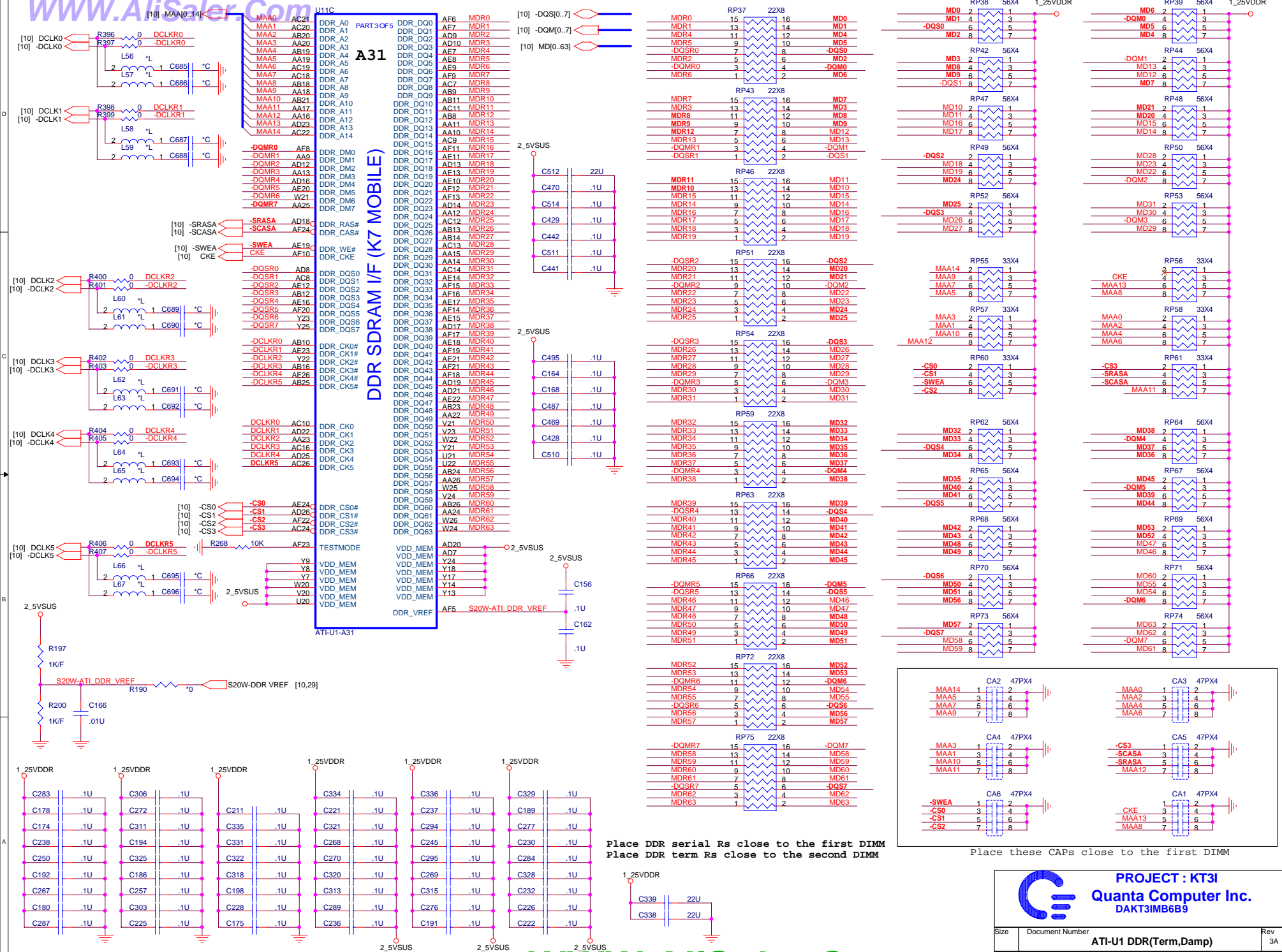
FID3	FID2	FID1	FID0	Clock Mode
0	0	0	0	1.1X
0	0	0	1	1.1.5X
0	0	1	0	1.2X
0	0	1	1	1.2.5X
0	1	0	0	5X
0	1	0	1	5.5X
0	1	1	0	6X
0	1	1	1	6.5X
1	0	0	0	7X
1	0	0	1	7.5X
1	0	1	0	8X
1	0	1	1	8.5X
1	1	0	0	9X
1	1	0	1	9.5X
1	1	1	0	10X
1	1	1	1	10.5X

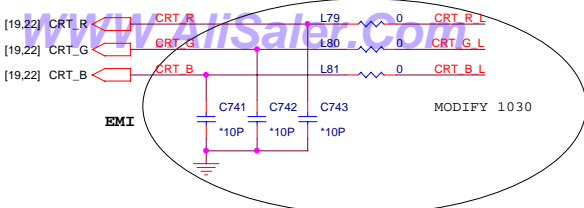
PROJECT : KT3I
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Size Custom Document Number CPU SOCKET A I Rev 3A

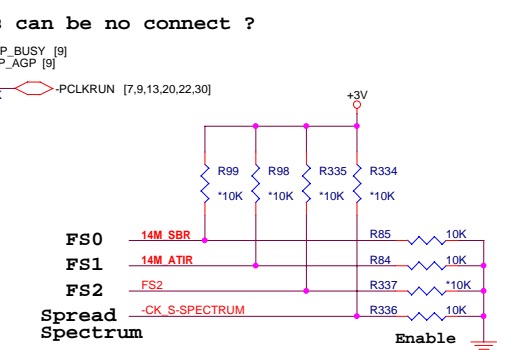
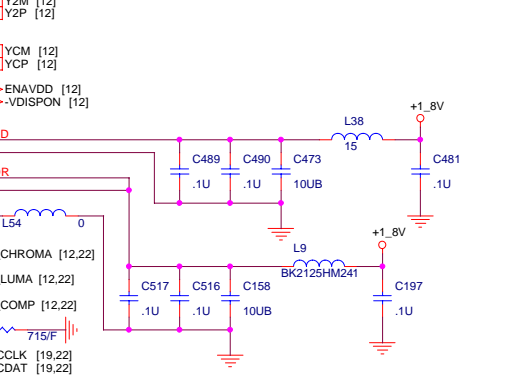
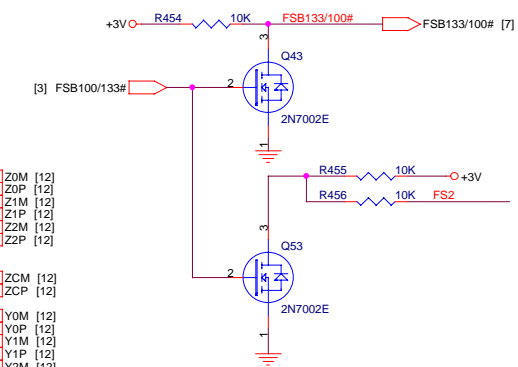
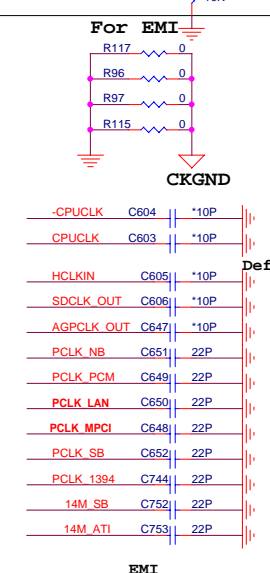
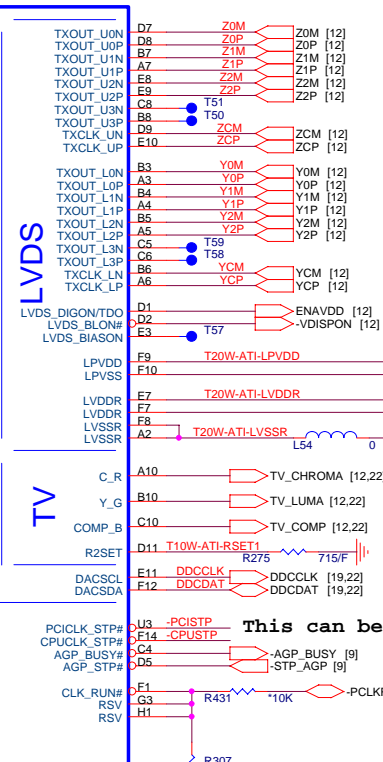
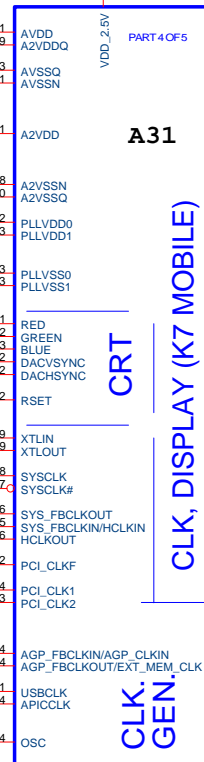
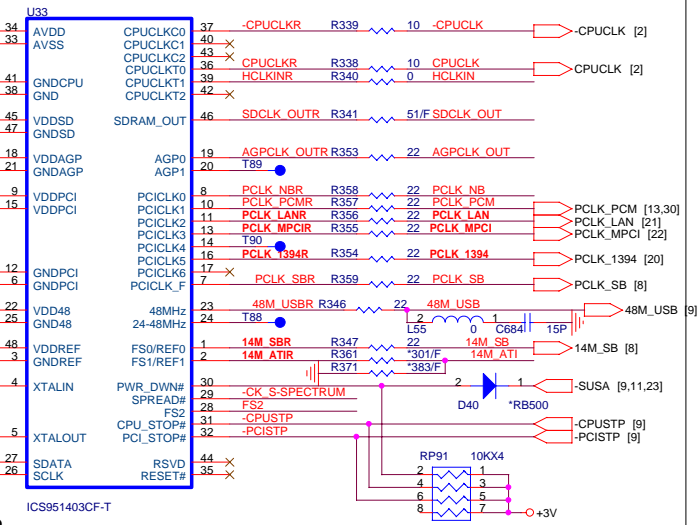
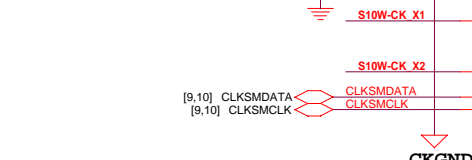
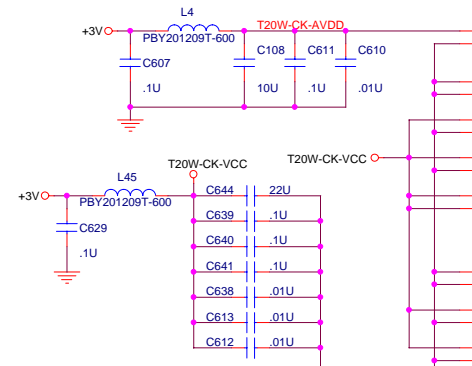
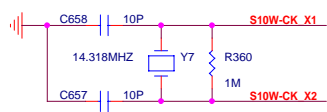
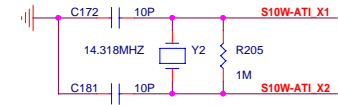
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Try to save the 14.318Mhz crystal for ATI after A-TEST

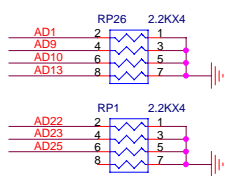
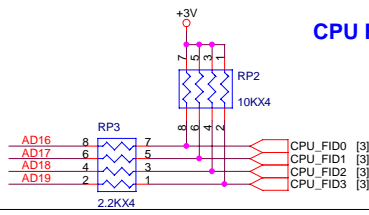
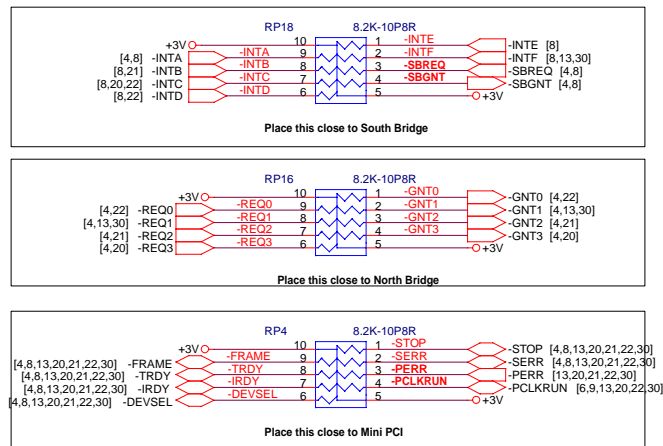


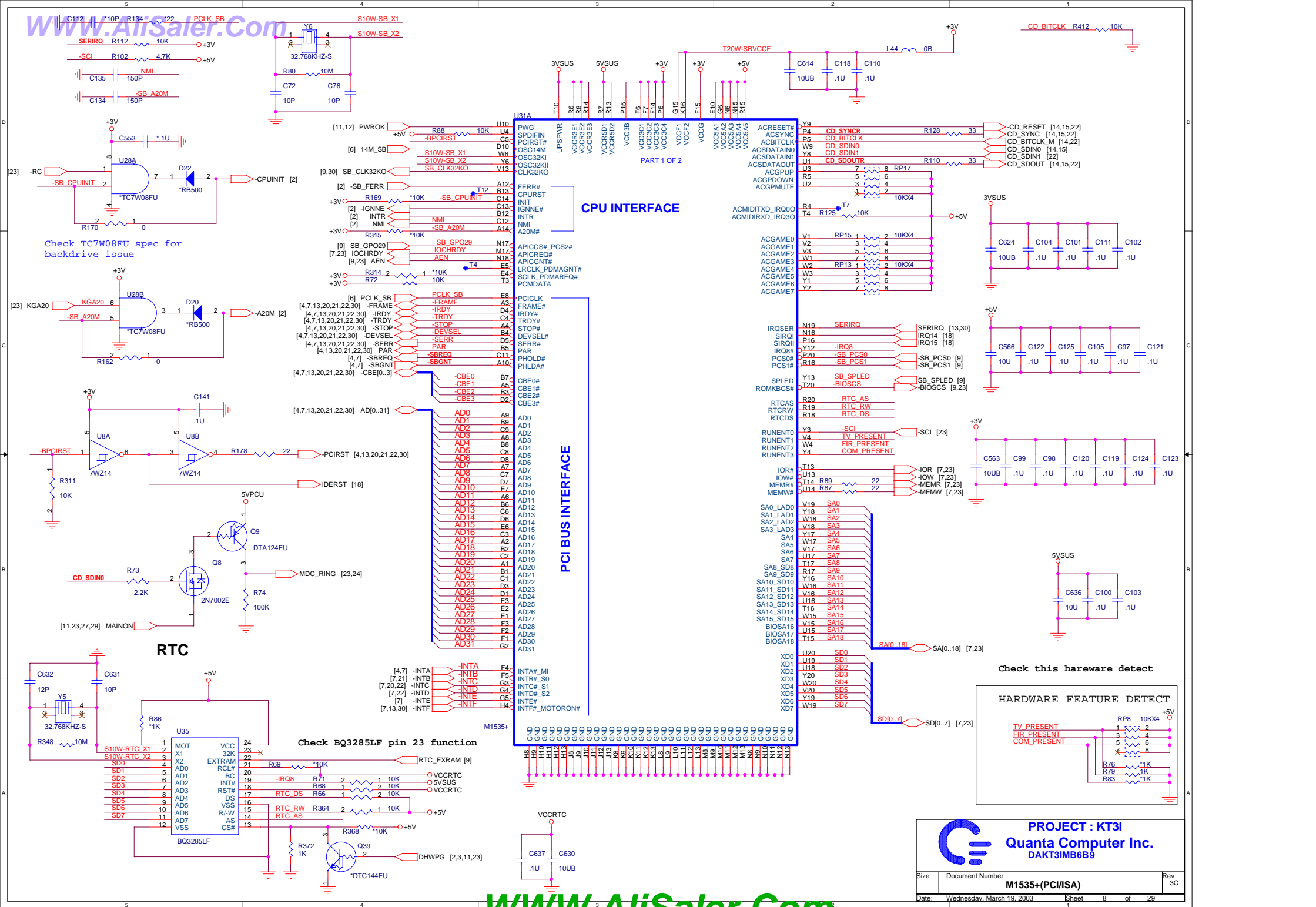
ICS951403 FREQUENCY SELECTION							
FS3	FS2	FS1	FS0	CPU	SDRAM	PCICLK	AGP SEL=0
0	0	0	0	100.00	100.00	33.33	66.67
0	0	0	1	100.00	133.33	33.33	66.67
0	0	1	0	100.00	150.00	30.00	60.00
0	0	1	1	100.00	66.67	33.33	66.67
0	1	0	0	133.33	133.33	33.33	66.67
0	1	0	1	125.00	100.00	31.25	62.50
0	1	1	0	124.00	124.00	31.00	62.00
0	1	1	1	133.00	100.00	33.33	66.67

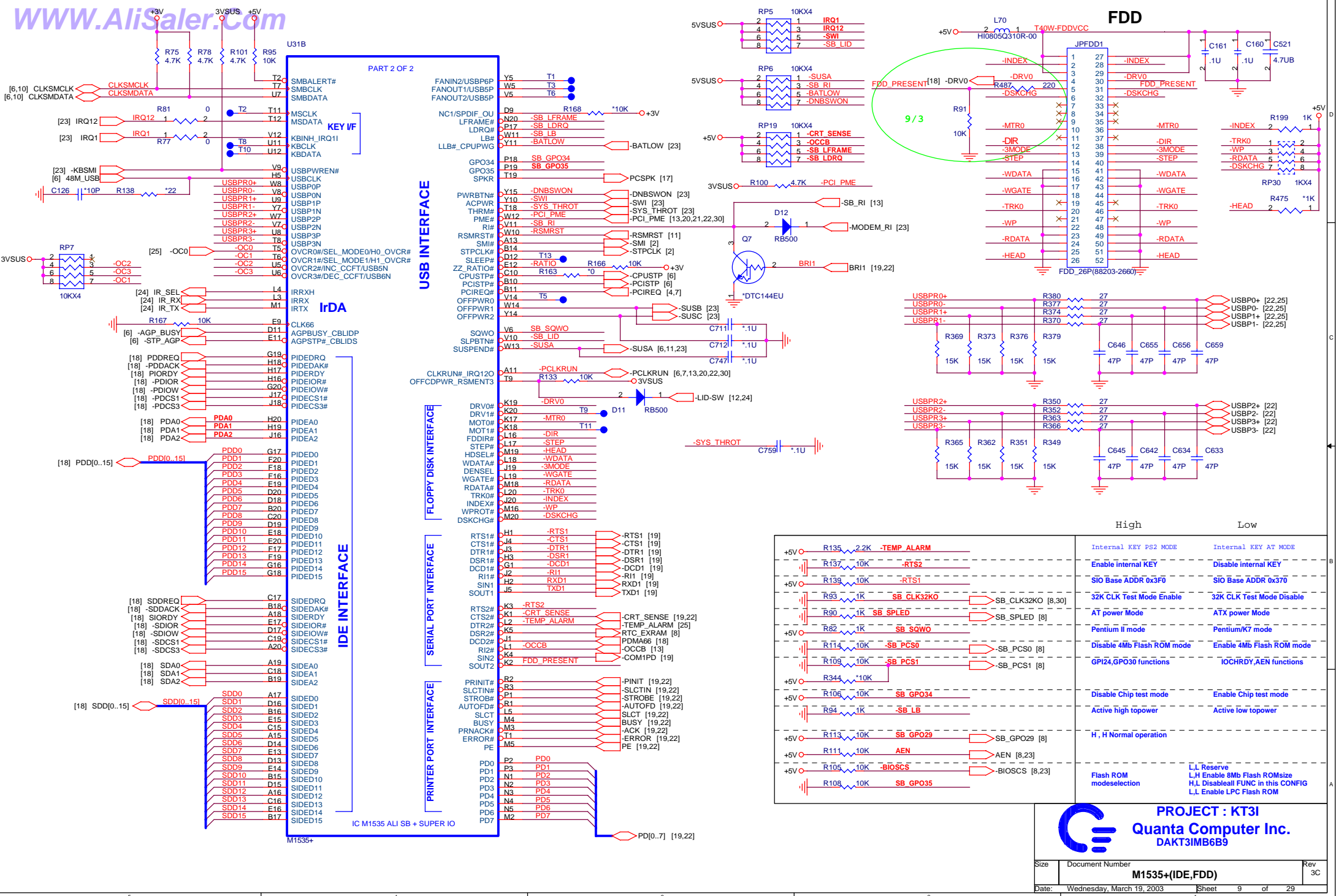


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Size	Document Number	Rev
	ATI-U1 VID,CRT,TV&CKGEN	3A
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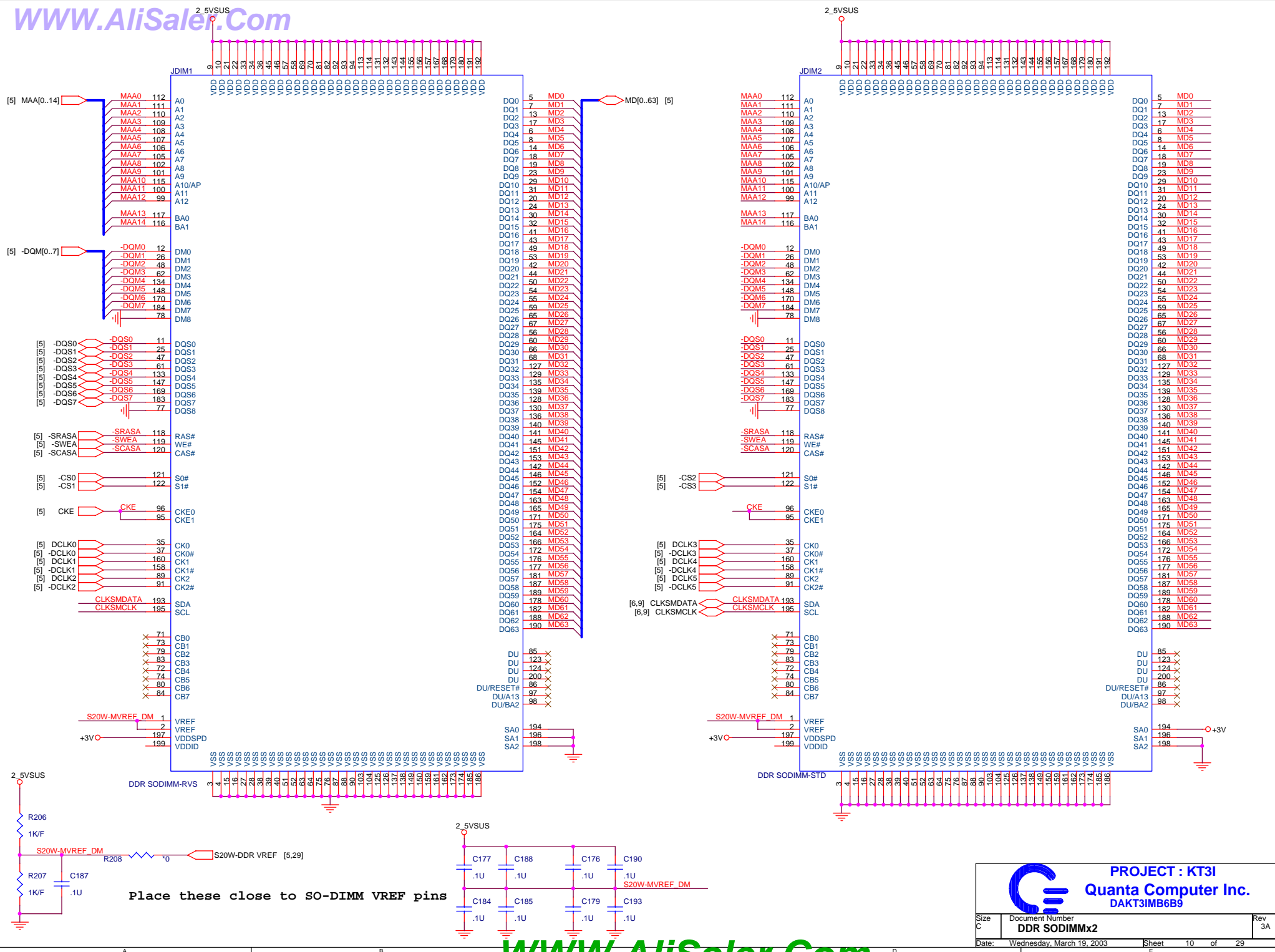


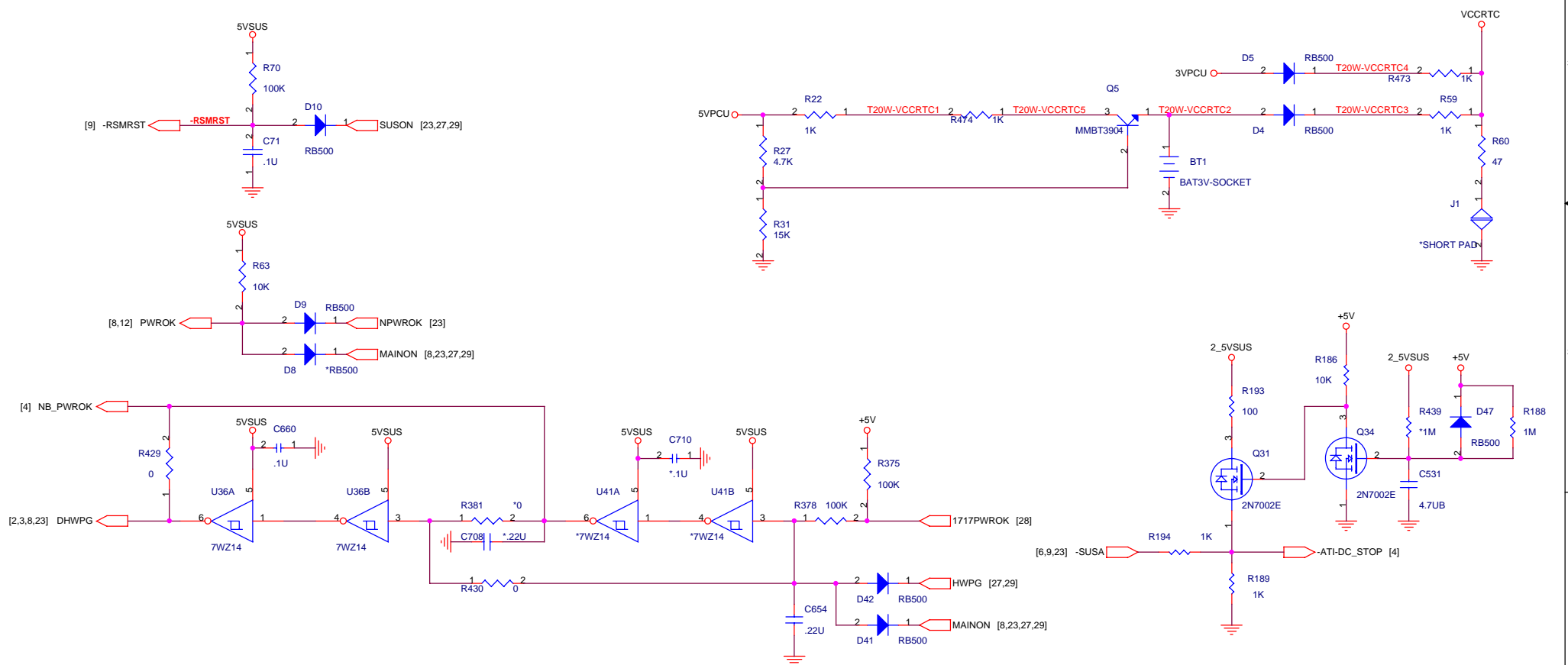
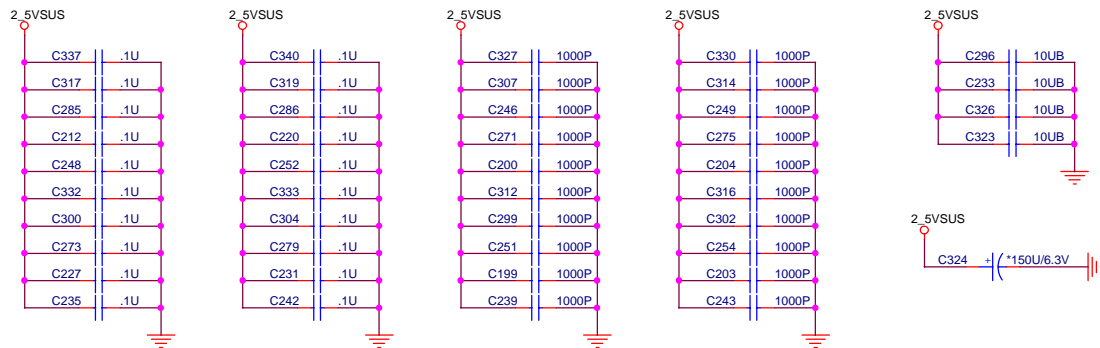


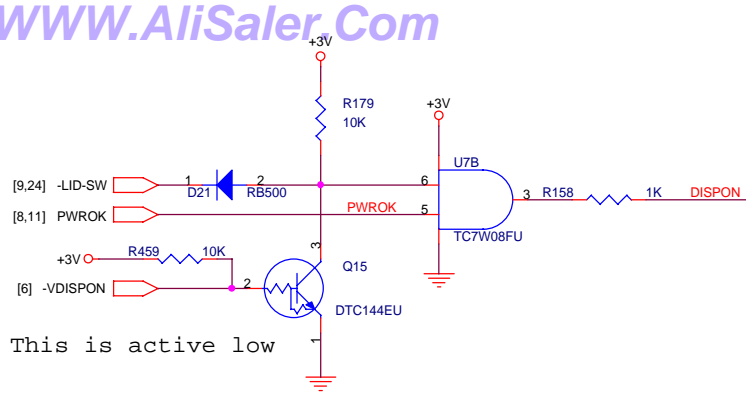


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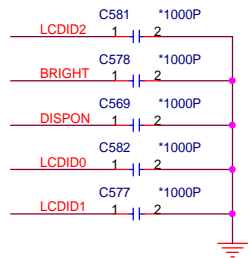
Size	Document Number	Rev
	M1535+(IDE,FDD)	3C
Date:	Wednesday, March 19, 2003	Sheet 9 of 29



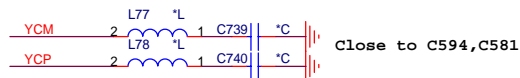




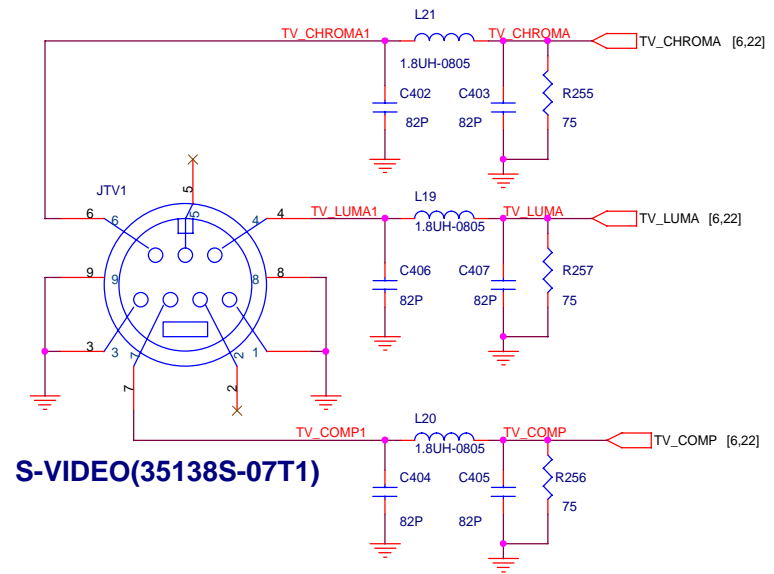
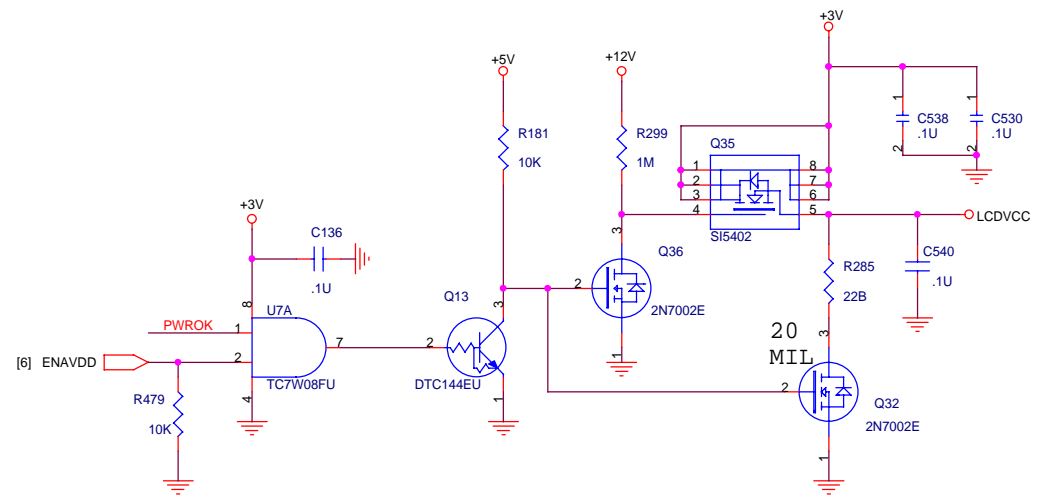
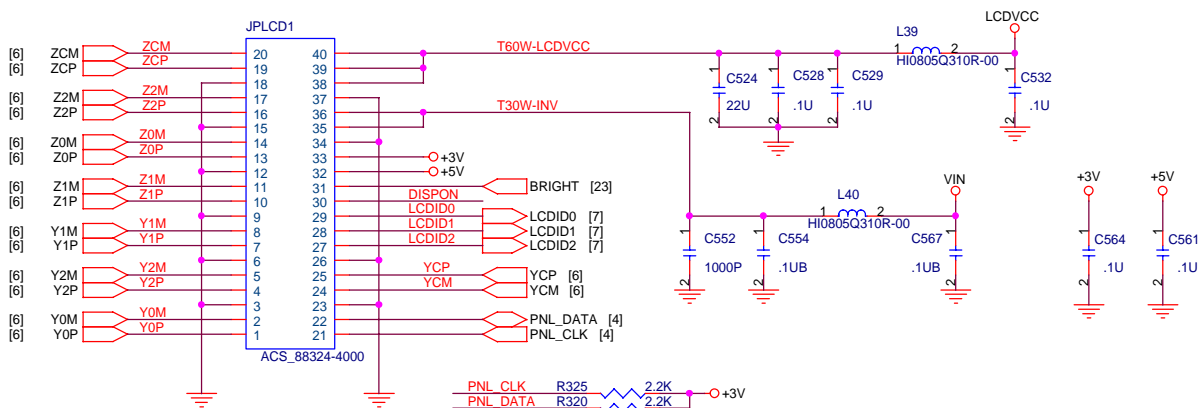
This is active low



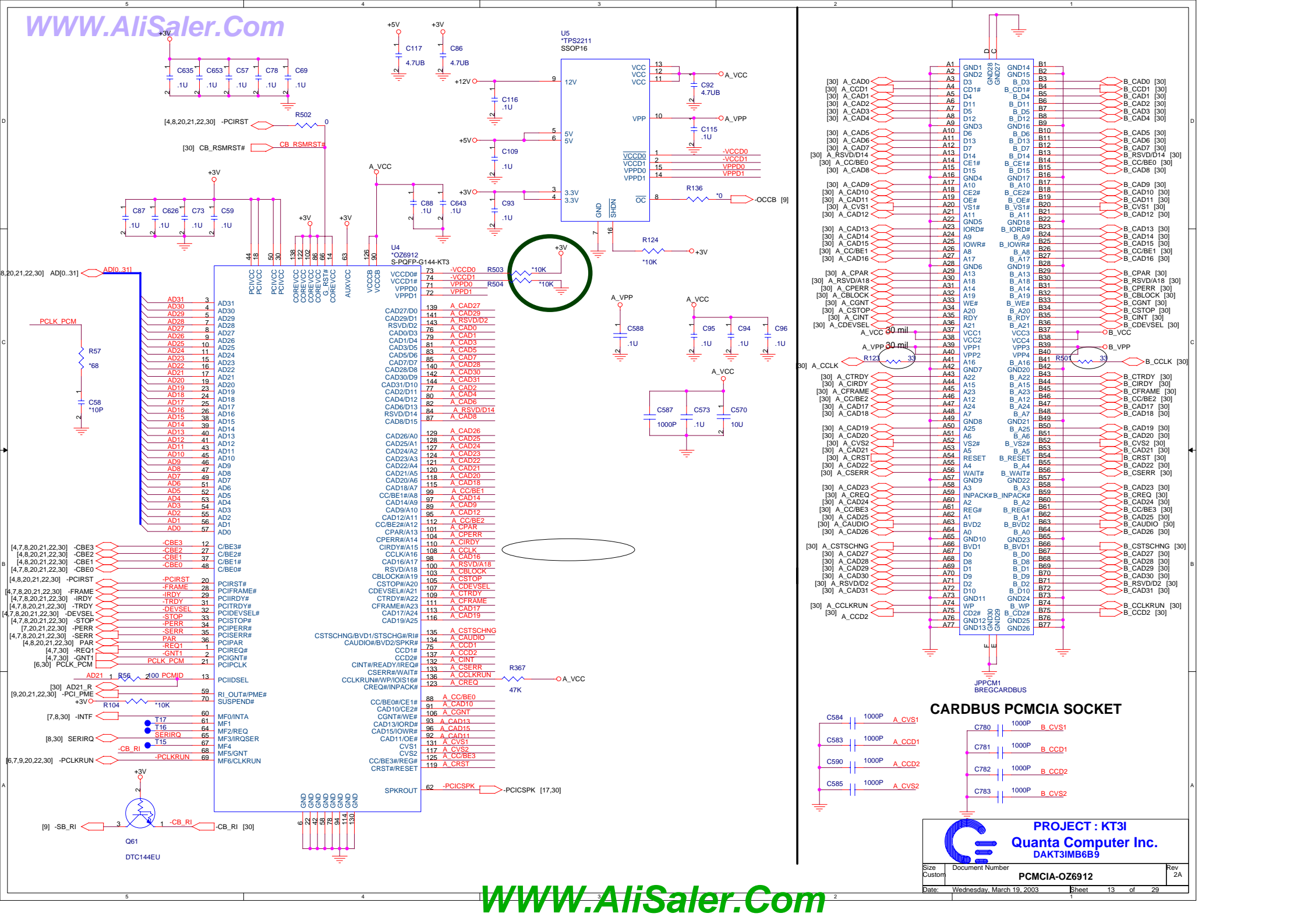
LCDID0	LCDID1	LCDID2	
1	1	1	Non SPWG XGA 1024*768 TFT
0	1	1	SXGA 1400*1050 TFT
1	0	1	SPWG XGA 1024*768 TFT



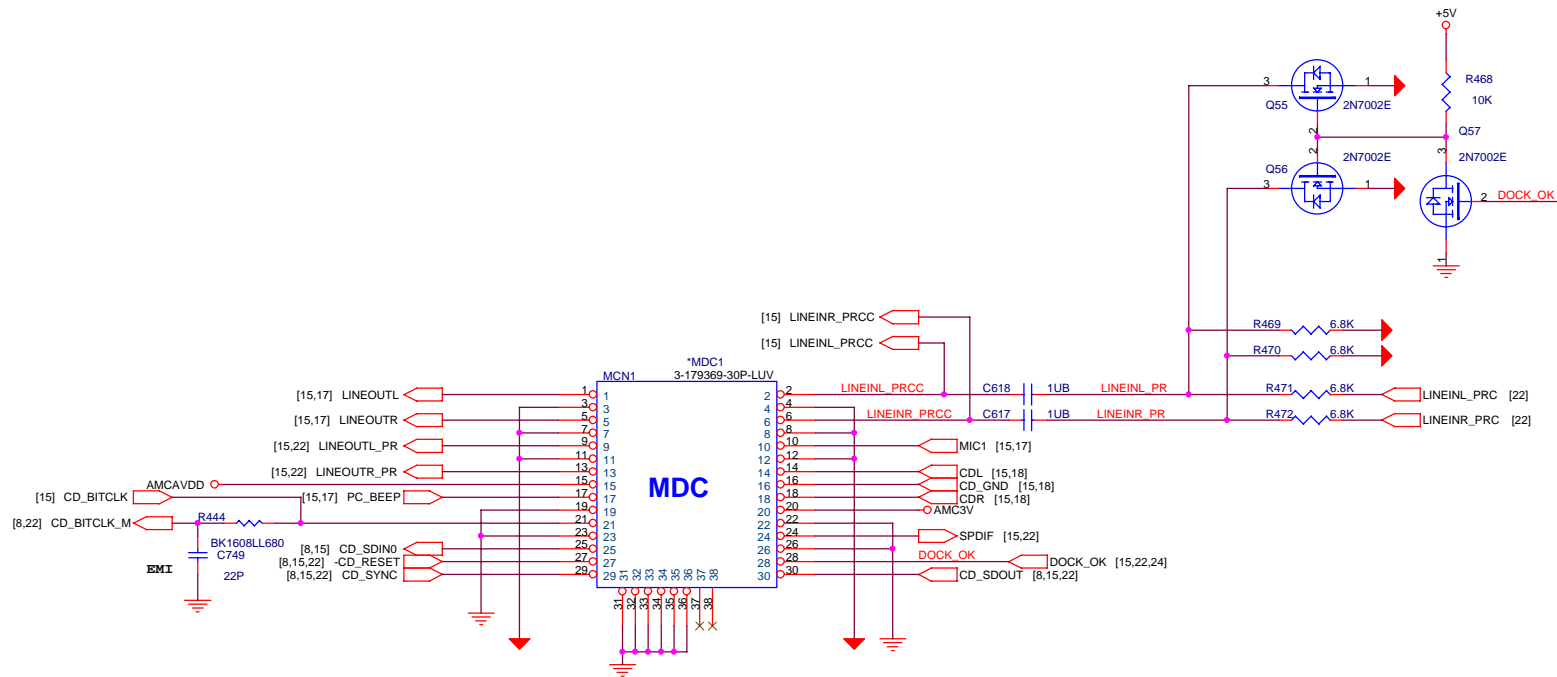
LCD INTERFACE



S-VIDEO(35138S-07T1)

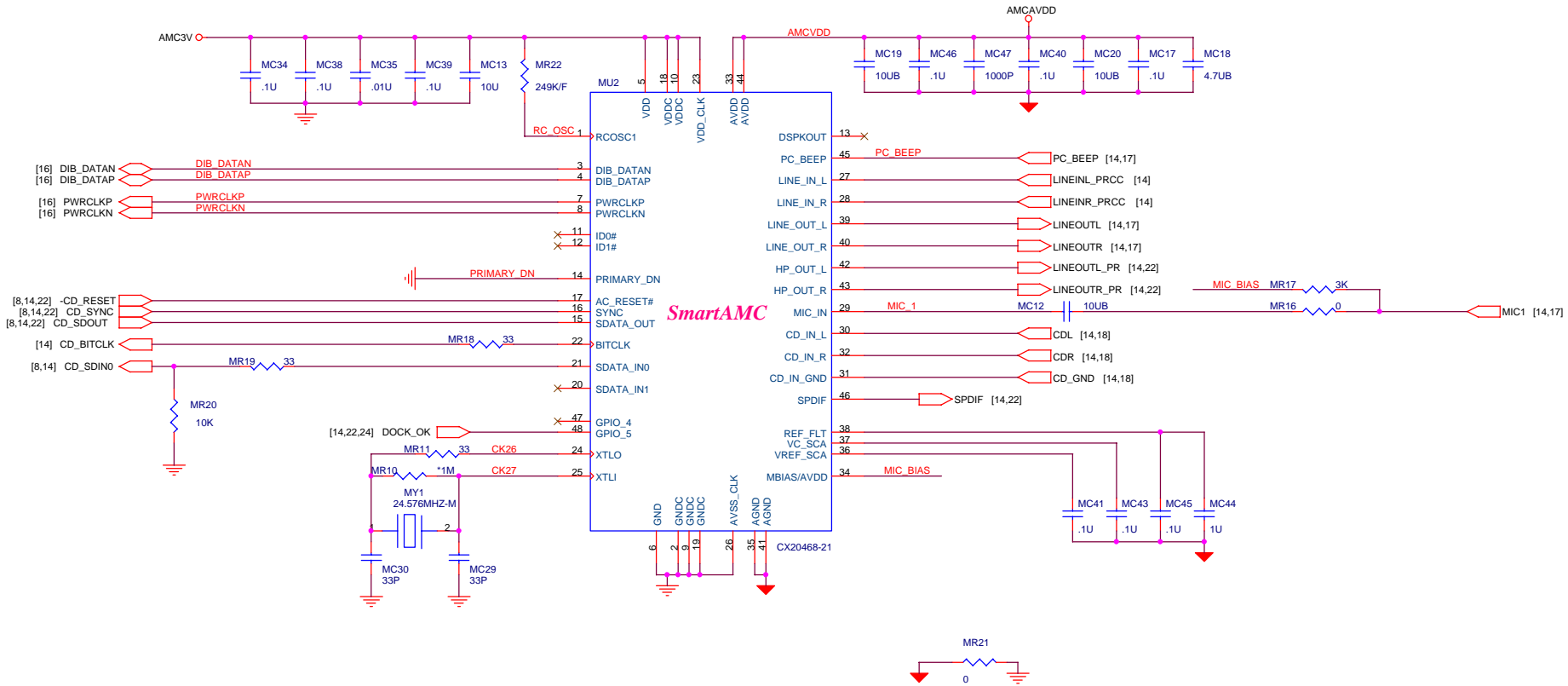


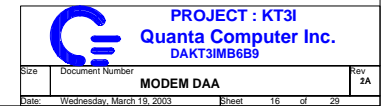
	DEFEATURE	FULL FUNCTION
R468	NO INSTALL	INSTALL
R469	0 OHM	6.8K
R470	0 OHM	6.8K
R471	NO INSTALL	INSTALL
R472	NO INSTALL	INSTALL
Q55	NO INSTALL	INSTALL
Q56	NO INSTALL	INSTALL
Q57	NO INSTALL	INSTALL



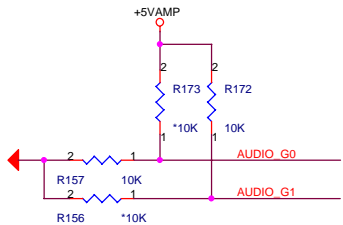
Treat MDC conn ,Smart DAA and CODEC as a daughter board

The AMC20493-001 modem is used for mother board family MBAMC20493-00.

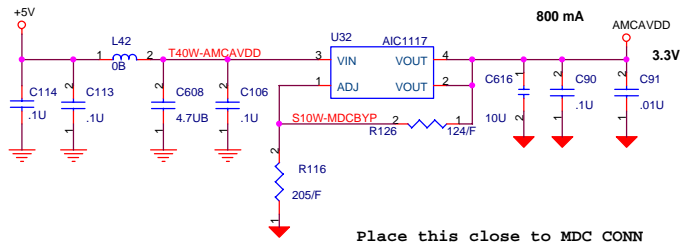
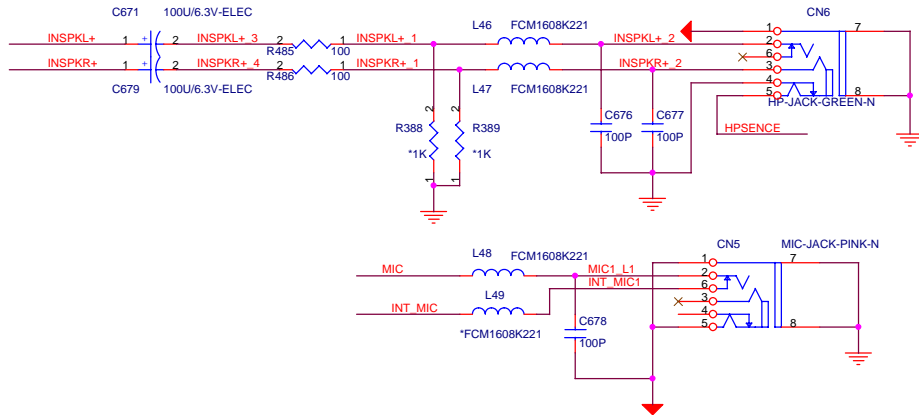
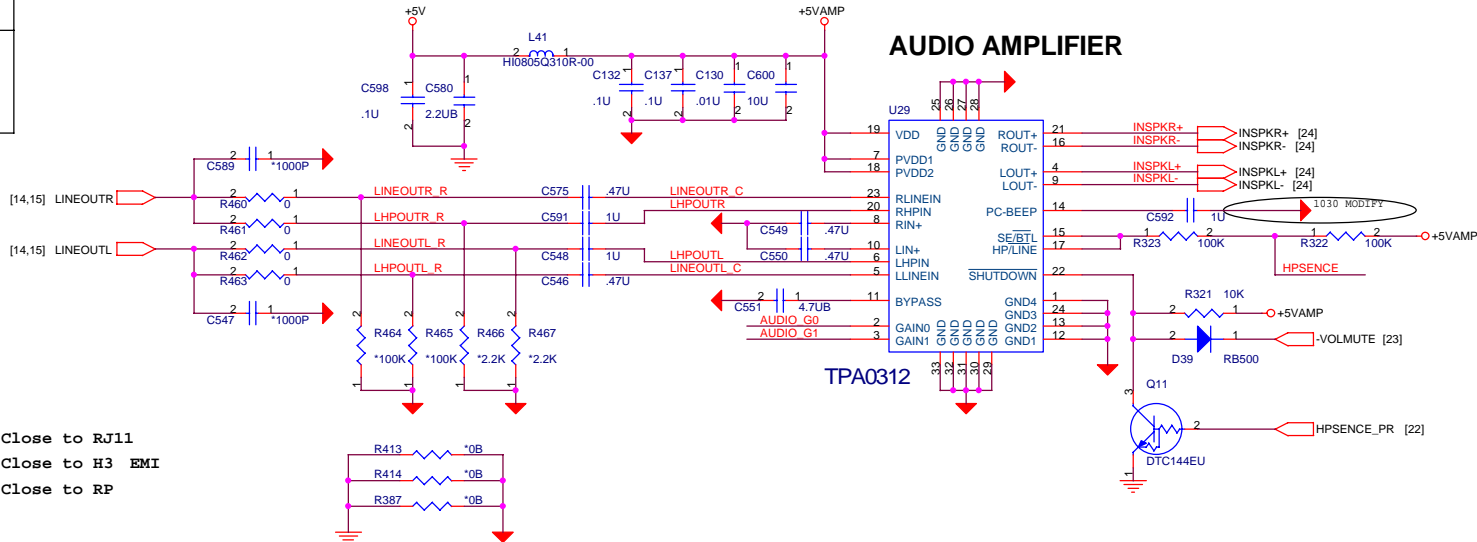
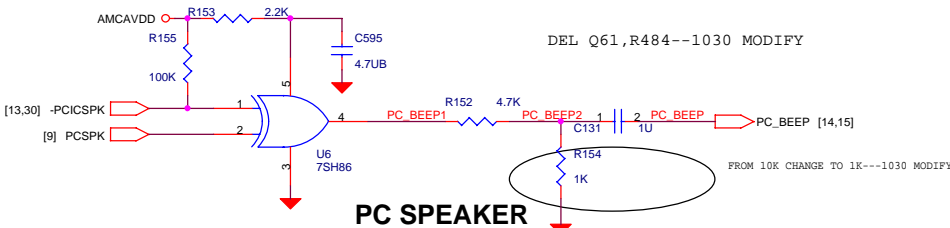
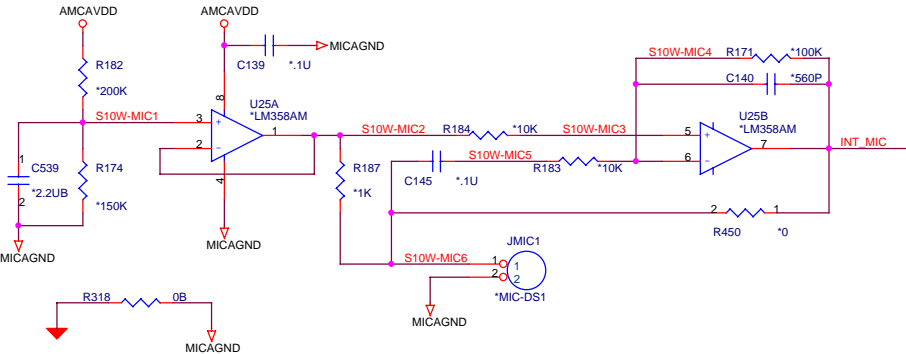
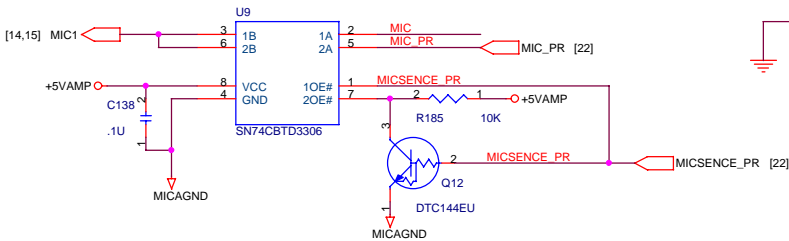




GAIN0	GAIN1	Av
0	0	6dB
0	1	10dB
1	0	15.6dB
1	1	21.6dB

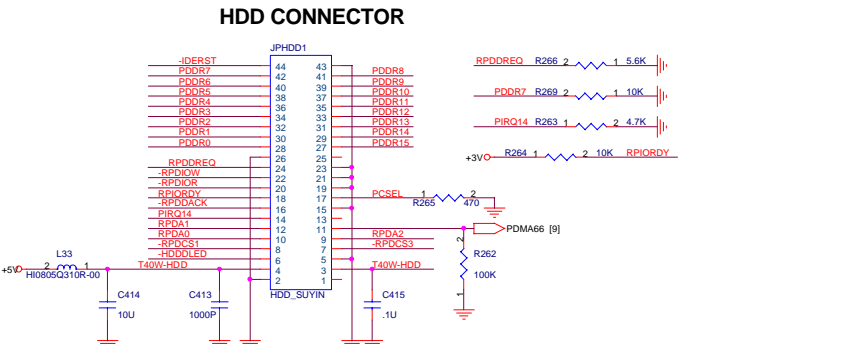
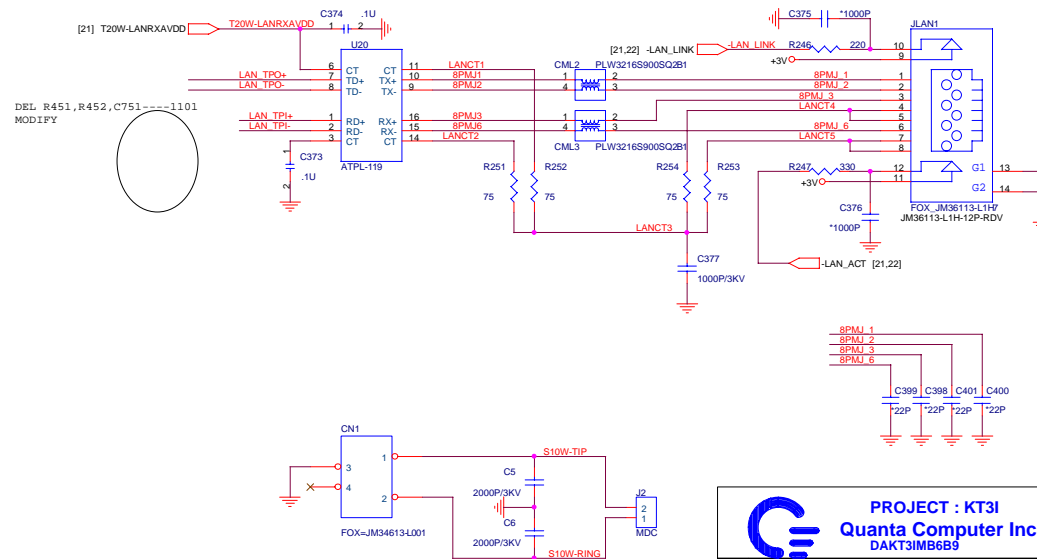
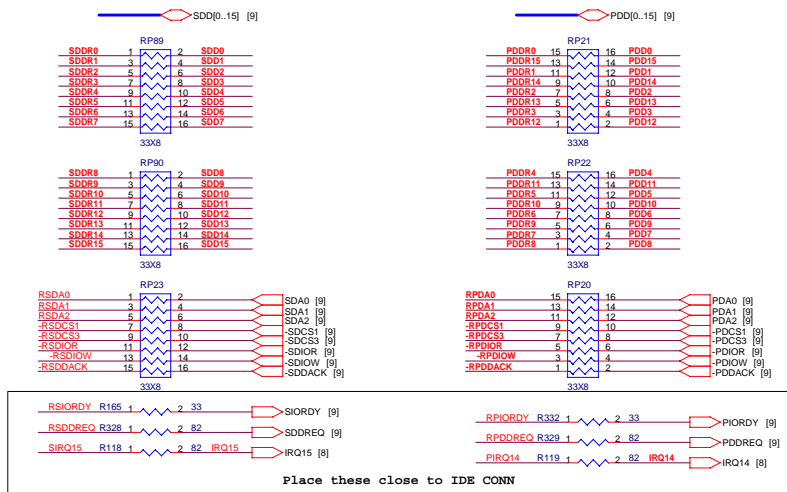
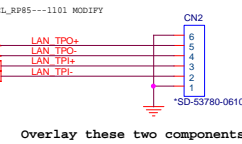
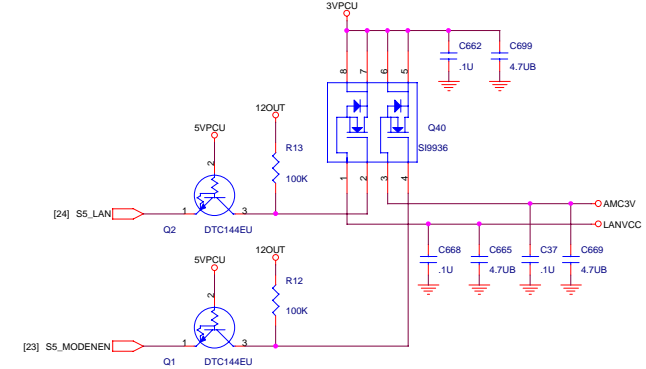


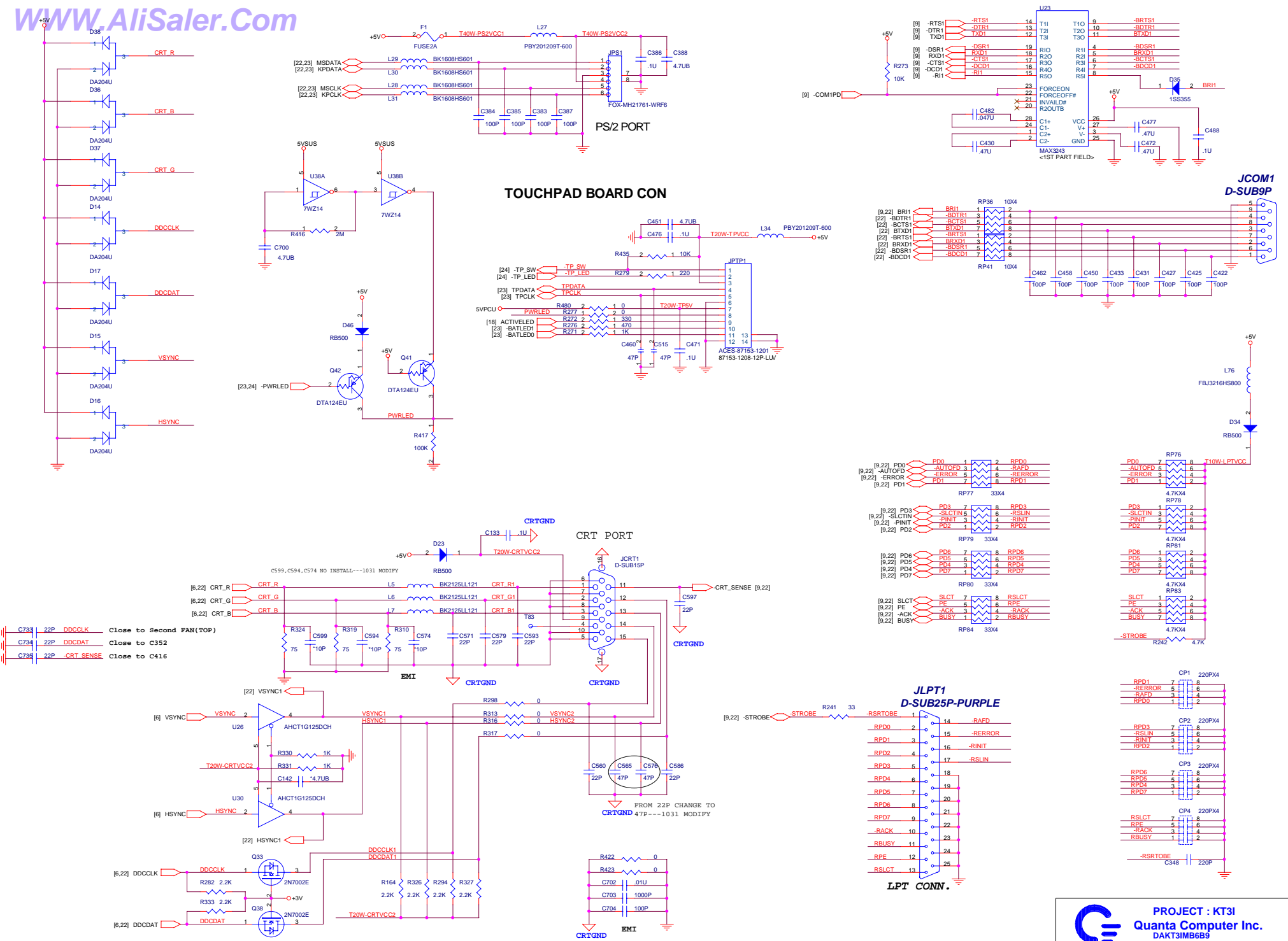
MIC_PR
Close to RJ11
Close to H3 EMI
Close to RP

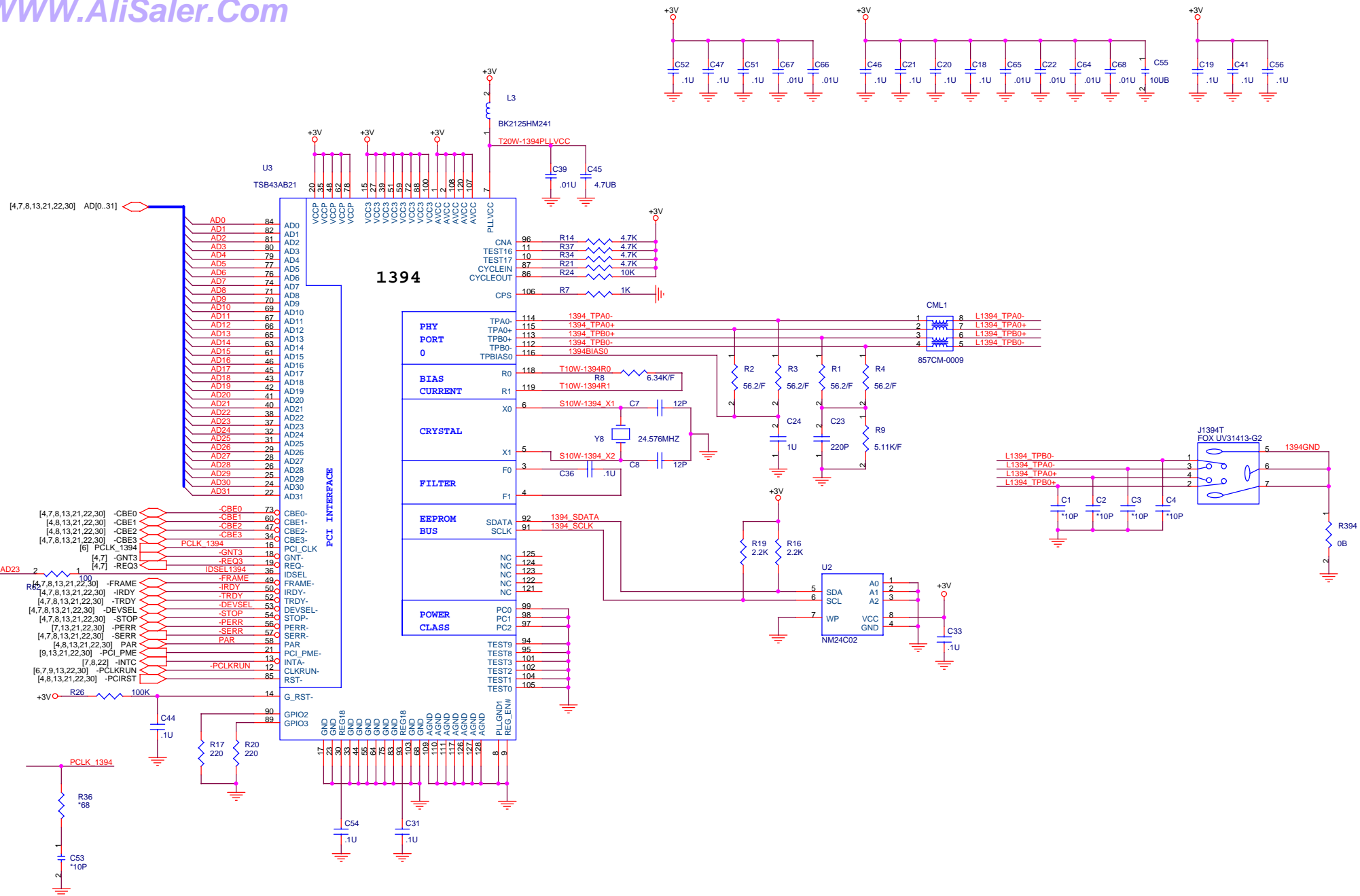


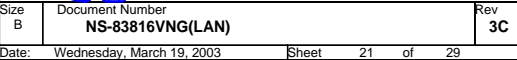
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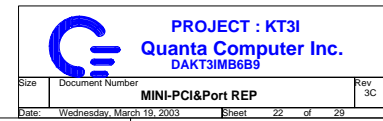
Size Custom	Document Number TPA0312&Audio Jack	Rev 3D
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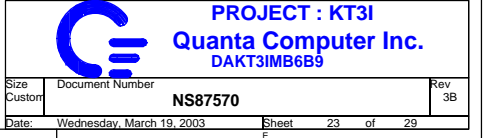


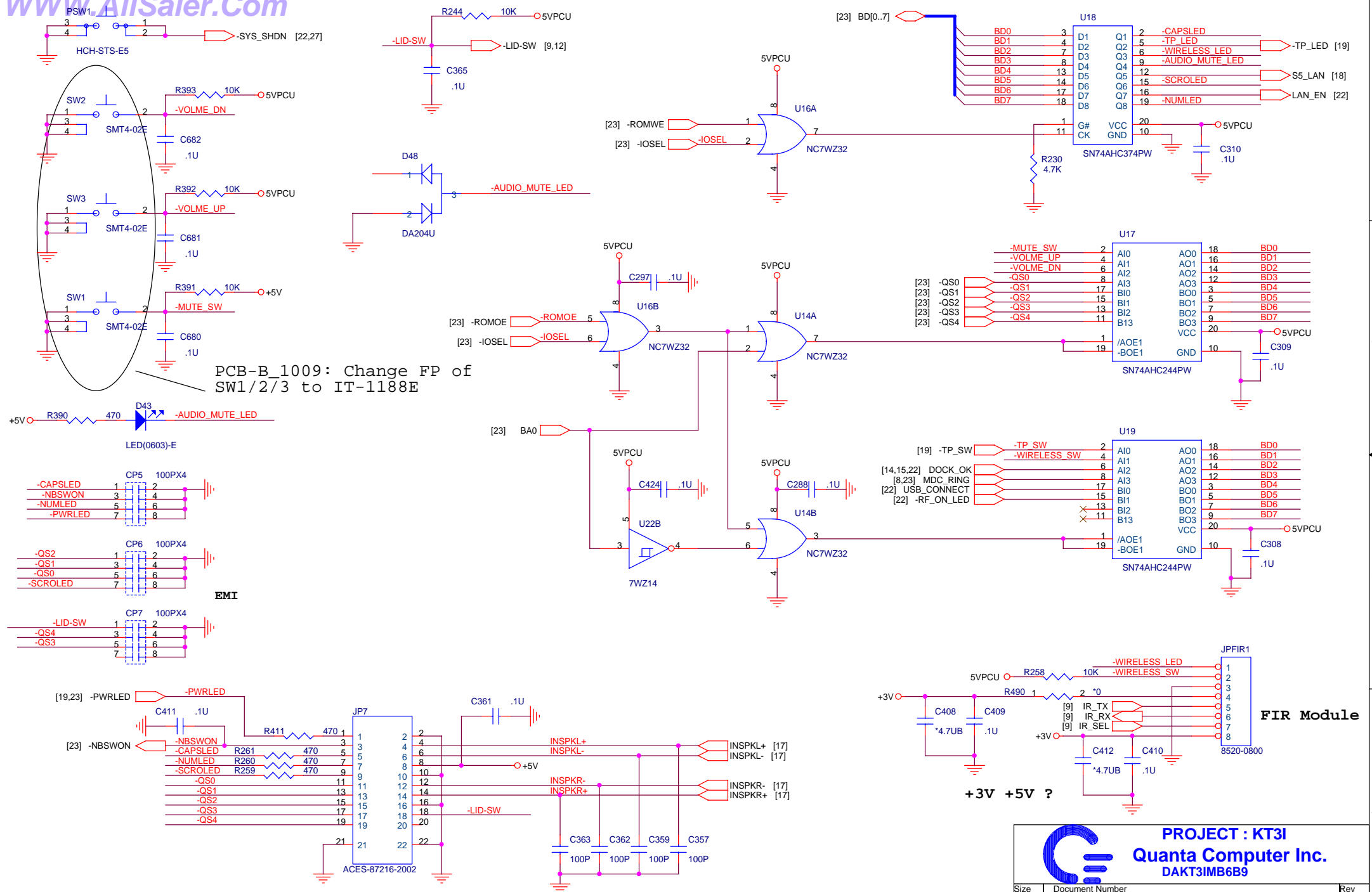


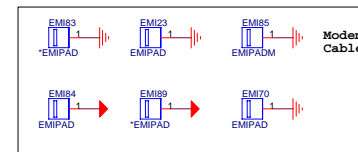
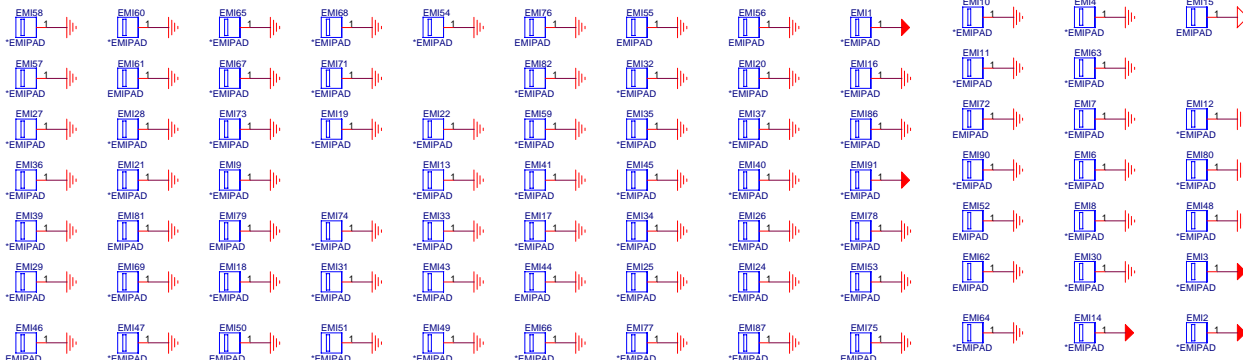
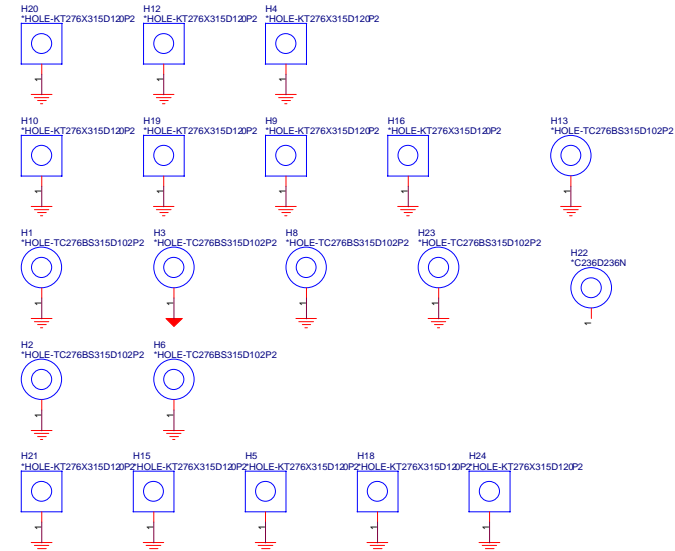
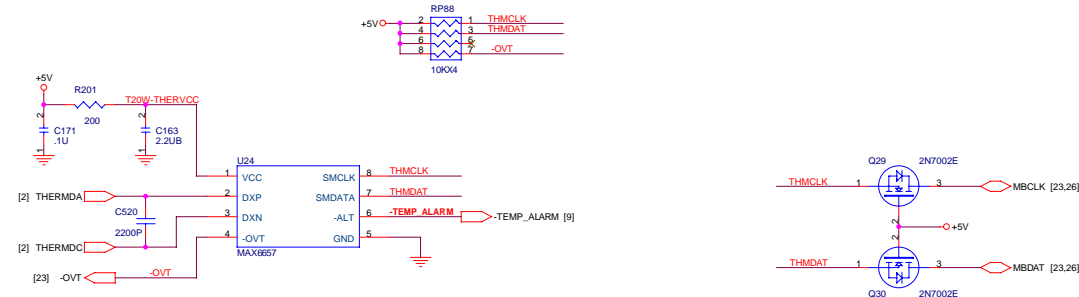
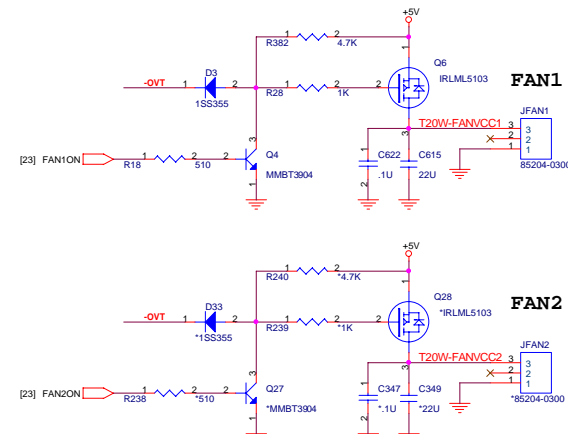
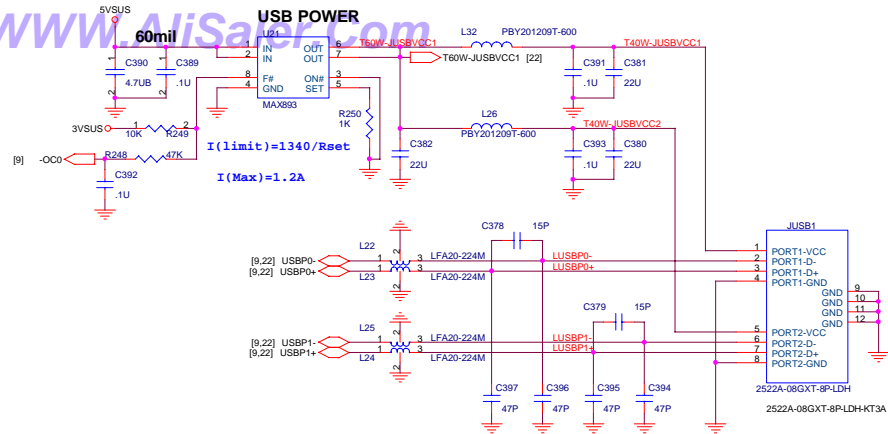


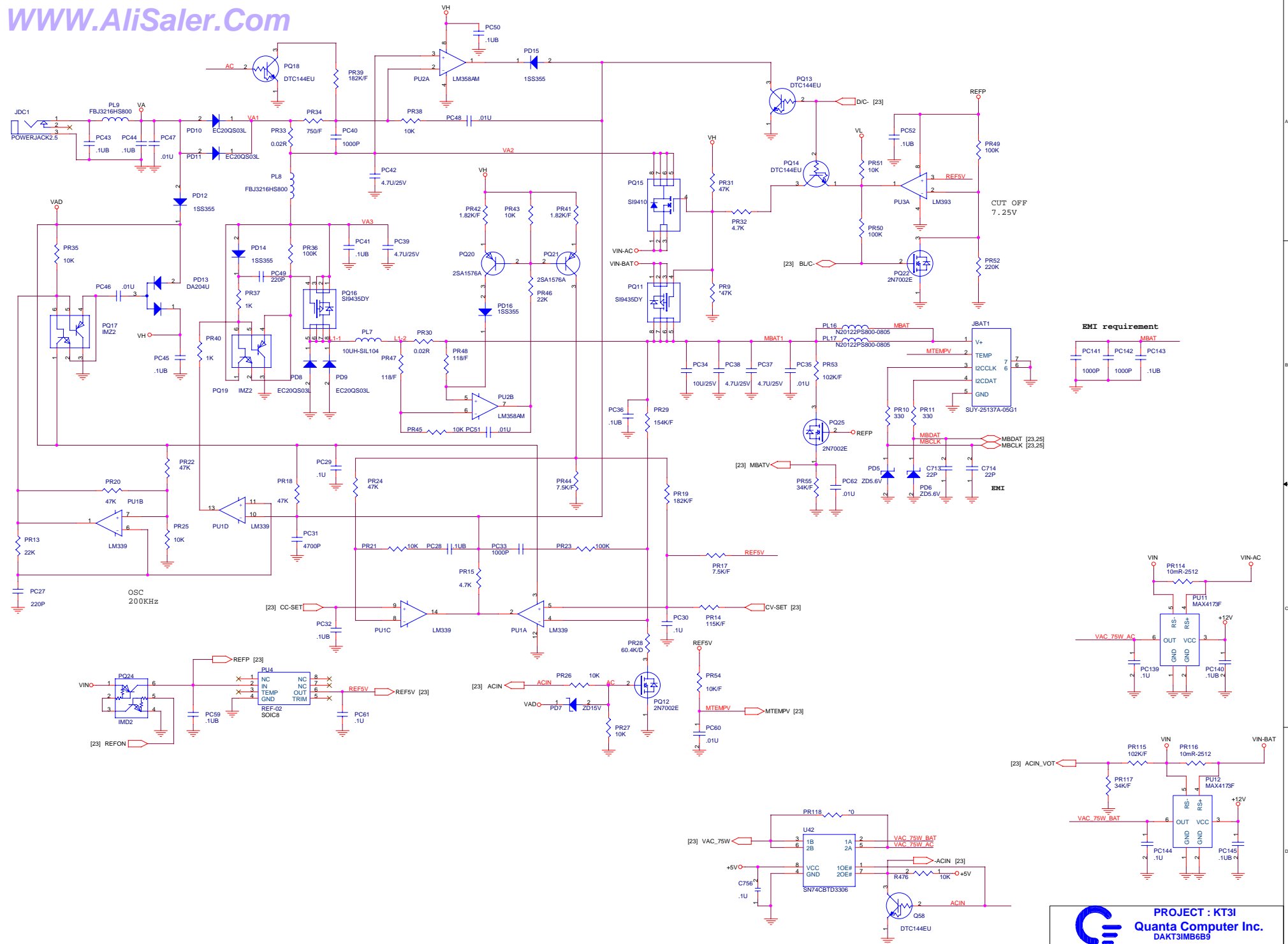


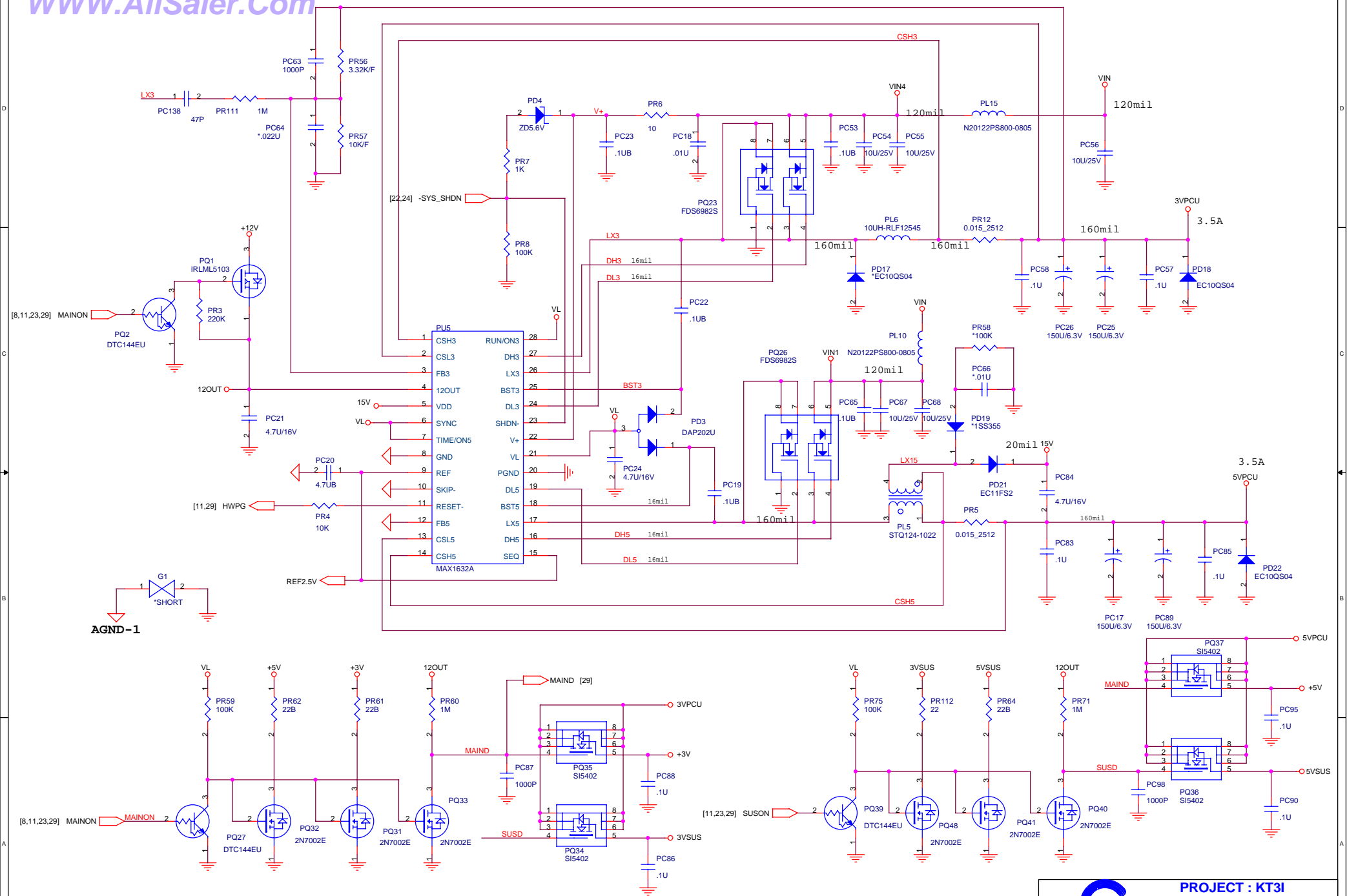


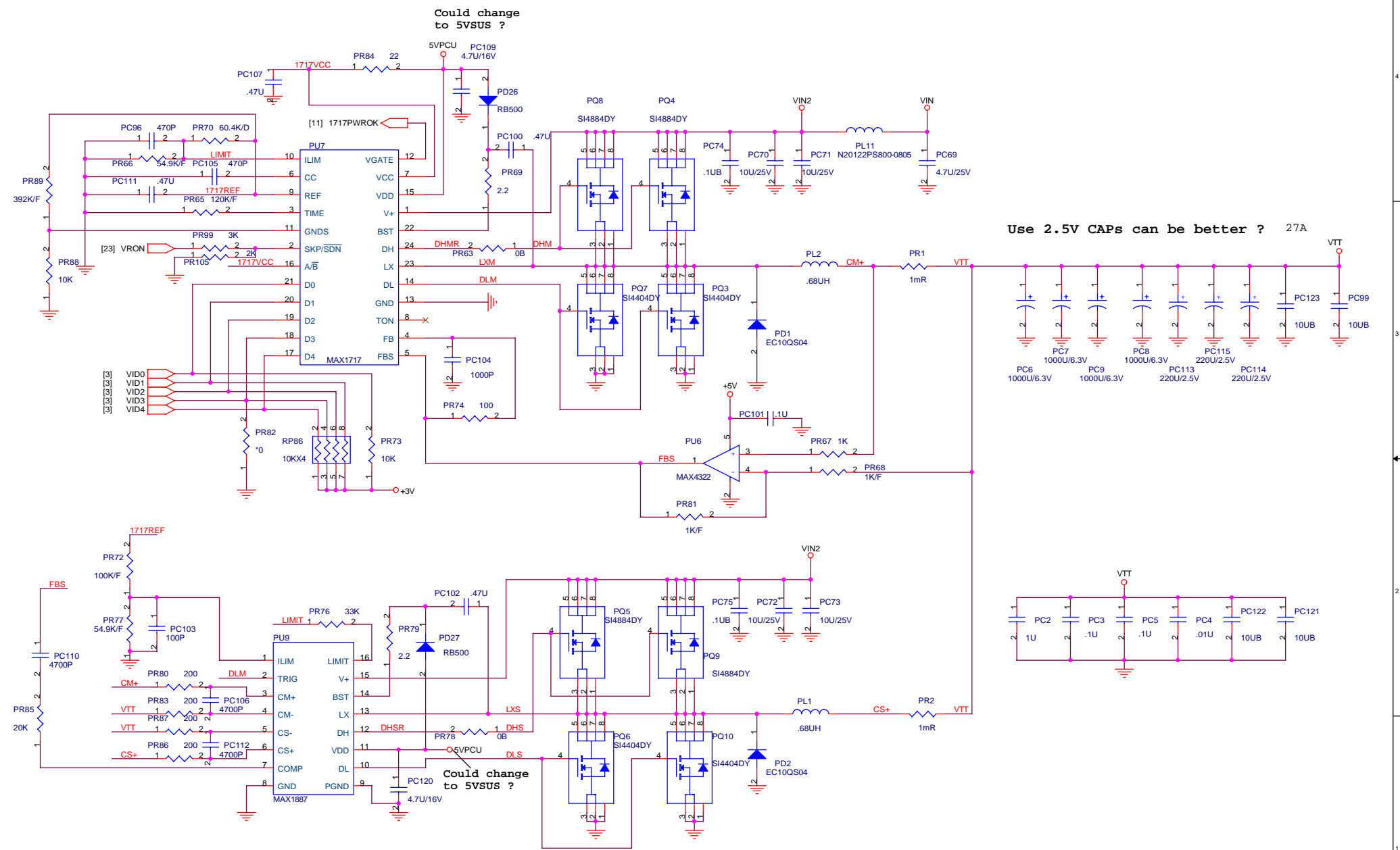


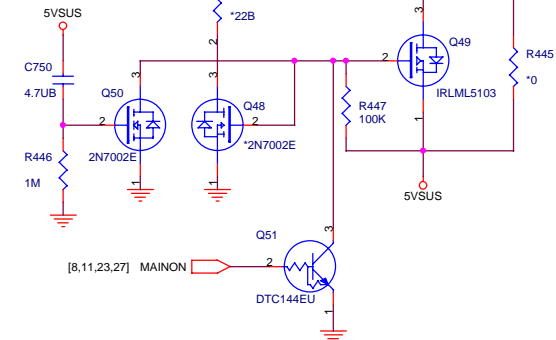
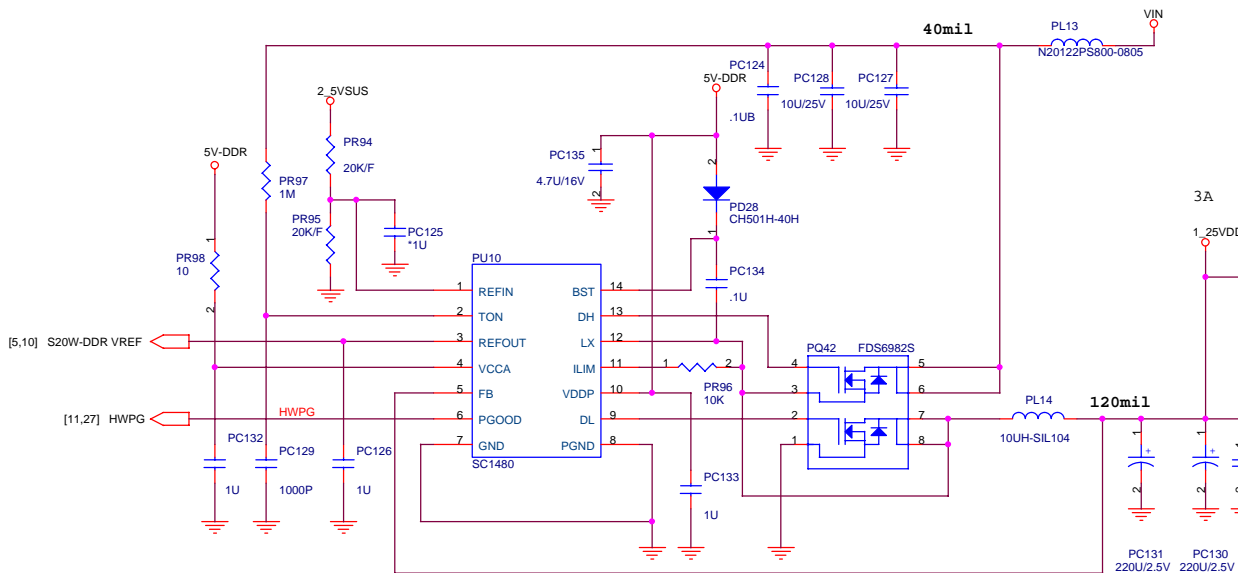
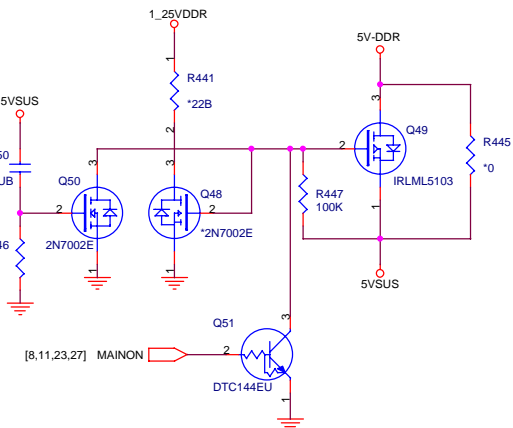
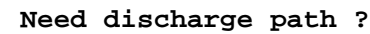












CONNECT PIN 117 TO A_SKT_VCC# = 026933 CONTROLLER INSTALLED
CONNECT PIN 117 TO PCI_RST# = ONLY SUPPORT D3_HOT
CONNECT PIN 117 TO GRST# = D3-COLD SUPPORT WITH GRST# SIGNAL

